

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of the Claims:

1. (Currently amended) A method for manufacturing a bonded wafer, comprising:

ion-implanting a light element into a wafer for active layer at a predetermined depth via an insulating film that has been formed thereon to form an ion-implanted area in said active layer wafer;

subsequently bonding said active layer wafer with a supporting wafer having an insulating film formed thereon together as their insulating films facing to each other to produce the bonded wafer; and

heat treating said bonded wafer to form bubbles of said light element in said ion-implanted area and thereby induce a cleavage and separation of a part of said bonded wafer defined in said ion-implanted side for forming an active layer wherein

a thickness of said insulating film of said active layer wafer, t_{dox} , satisfies the following formula:

$$t_{dox} < (1/9) \times t_{soi},$$

where t_{soi} = thickness of said active layer, and wherein the thickness of said active layer is approximately 333 nm or less.

2. (Cancelled)

3. (Previously presented) A method for manufacturing a bonded wafer in accordance with claim 1, in which

said active layer wafer and said supporting wafer are subjected to a plasma treatment, respectively, before said bonding of said active layer wafer with said supporting wafer.

4. (Cancelled)

5. (Cancelled)

6. (Cancelled)

7. (Currently amended) A method for manufacturing a bonded wafer in accordance with claim 1, in which the thickness of said insulating film of said active layer is between ~~0.05- μm~~ 10 nm and ~~1.0- μm~~ 30 nm.

8. (Currently amended) A method for manufacturing a bonded wafer in accordance with claim 3, in which the thickness of said insulating film of said active layer is between ~~0.05- μm~~ 10 nm and ~~1.0- μm~~ 30 nm.